

All-Optical Decrementing of a Packet's Time-to-Live (TTL) Field and Subsequent Dropping of a Zero-TTL Packet

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Abstract—We demonstrate an optical time-to-live (TTL) decrementing module for optical packet-switched networks. Our module acts on a standard NRZ-modulated binary TTL field within a 10 Gb/s packet and decrements it by one if the TTL is nonzero. If the TTL of the incoming packet is zero, the module signals an optical switch to drop the packet. Our technique is independent of the TTL length, does not require the use of ultrashort RZ optical pulses, requires no guard time between the end of the TTL field and the packet data, and has only a 2.4 dB power penalty at 10^{-9} bit-error rate.

Index Terms—Optical communications, optical networking, optical TTL, time-to-live (TTL).

I. INTRODUCTION

THERE has been increased recent interest in the vision of an optical packet-switched network. In these networks, packets are processed and switched optically, without need for conversion of the optical packets to electronic signals at intermediate nodes within the network core. However, while all-optical packet switching remains a laudable goal for efficient and high-throughput networking, a move to optical packet switching within the network core may require implementation of a number of packet-processing and network-management functions currently performed by electronic routers, in the optical domain – a fact frequently ignored when the concept of the “all-optical network” is discussed.

Manuscript received December 17, 2002; revised July 25, 2003. This work was supported in part by the National Science Foundation and the Intel Corporation.

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Digital Object Identifier 10.1109/JLT.2003.819131

One problem faced by many packet-switched networks is that of “routing loops,” where misdirected or mislabeled packets are routed in circles, never reaching their destination, and leading to severe network congestion [1]. While rare packet processing errors can result in misrouted packets, routing loops can occur due to errors within the routing table stored at a switching node, or because a change in the routing table is taking longer than normal to propagate throughout the network. One commonly-employed method to prevent these loops from strangling the bandwidth of a network is to use a “time-to-live” (TTL) field within the header of a packet. The TTL field determines the maximum numbers of hops a packet can take before getting dropped for being too “old” for the network. In modern IP packets, the TTL is a binary number (typically 8 b long) that is decremented by 1 when traversing a switching node. When the TTL value of a packet reaches zero, the packet is dropped from the network. “Rogue” packets are either eventually rerouted toward the correct destination, or the TTL expires and they are dropped. Decrementing this TTL field requires editing the packet header, something easy in electronics but often difficult in optical systems.

There has been minimal previous research on optical technologies for decrementing the TTL field of an optical packet, or of dropping or destroying packets that have a zero TTL, particularly for NRZ systems. There has been one recent report of using a discrete series of ultrashort RZ optical pulses as a packet’s “TTL burst,” where instead of a binary field within the header the number of optical pulses present corresponded to the value of the TTL [2]. The TTL module, consisting of a semiconductor-optical-amplifier (SOA) and a mode-locked laser, would extinguish a single pulse each time the packet passed through the module. When no pulses remained, the packet was dropped. However, this method required restructuring of a standard packet header as the binary TTL field was replaced with an indeterminate number of optical pulses.

We demonstrate a module that acts upon a standard 8-b TTL field located within an NRZ-modulated optical packet. If the TTL field within the incoming packet is nonzero, the packet passes through the module, where gain saturation within an SOA and difference-frequency-generation (DFG) in a set of periodically poled lithium-niobate (PPLN) waveguide wavelength shifters are used to decrement the TTL field. If the

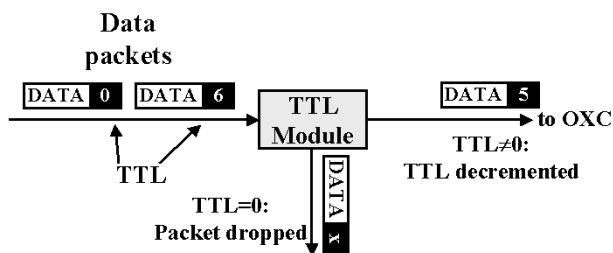


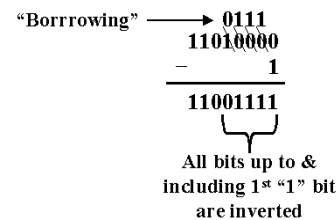
Fig. 1. A conceptual diagram of our optical time-to-live (TTL) decrementing module. A packet enters a switching node and first passes through the TTL module. If the TTL is nonzero, it is decremented by one and passes to the switch fabric. If the TTL is zero, it is dropped from the network and the resulting TTL value is irrelevant.

TTL field of the incoming packet is zero, the module processes the packet and generates a control signal that is used to drop the packet from the network. Our technique is independent of the length of the TTL and the packet, requires minimal control electronics, does not require the use of ultrashort RZ optical pulses, requires no guard time between the end of the TTL field and the rest of the packet, and has only a 2.4-dB power penalty at 10^{-9} bit-error rate (BER). In addition, while our module is experimentally demonstrated for 10 Gb/s NRZ-modulated packets, it can, with minimal modification, be made compatible with RZ-modulated data.

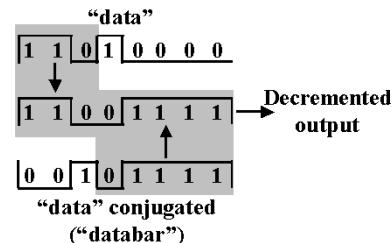
II. SYSTEM CONCEPT

A conceptual diagram of how our TTL module might operate within an optical switching node is shown in Fig. 1. A packet with a binary TTL field of known length (commonly 8 bs) enters the switching node and the TTL module. The TTL module checks the TTL of the incoming packet – if it is nonzero, the TTL is decremented by one and the packet passes through to the optical switch fabric. If the TTL of the incoming packet is zero, the packet is destroyed or dropped. In the zero-TTL case, the resulting TTL value after passing through the module is irrelevant as the packet is no longer within the network.

Optically decrementing the TTL by “1” requires optical implementation of a binary subtraction algorithm [3], [4]. One such method is decrementing-via-inversion. For any arbitrary starting binary value, subtracting “1” results in the least-significant-bit (LSB) being inverted (in binary subtraction, 1 minus 1 is 0, while 0 minus 1 results in a 1, after “borrowing” from the next higher column). If the LSB is a “1” bit, the subtraction is complete, as no borrowing is necessary. However, if the LSB is a “0” bit, we must reduce the next higher column by “1” as a result of the borrowing process (and if that next column is “0”, leads to additional borrowing). This process is illustrated in Fig. 2(a). To decrement by one, invert each bit, beginning with the LSB, until a “1” bit is encountered. Invert the “1” bit, and then stop (as inverting the “1” bit completes the subtraction). All bits beyond the first “1” bit remain unchanged. This process results in a set of bits being replaced by their conjugates, and thus this binary subtraction can take place by replacing bits, up-to-and-including the first “1” bit, with their conjugates, as shown in Fig. 2(b) – the TTL data to be decremented in this figure consists of the bit sequence “11 010 000” (MSB → LSB). To create the decremented output, the first 5 bits (starting with the LSB, up to and



(a)



(b)

Fig. 2. (a) Binary subtraction of “1” from the 8-b binary value “11 010 000”. Due to the borrowing process inherent to subtraction, the result, “11 001 111” has all bits, up to and including the first “1” bit, inverted. (b) Subtracting one from a binary number can be performed by replacing a series of bits (up to and including the first “1” bit) with their conjugates.

including the first “1” bit) are replaced with their conjugate (the shaded part of “databar”), while the rest of the bits remain the same (the shaded part of “data”). To implement this “decrementing-via-inversion” process in the optical domain, there are three main requirements: 1) a method of generating a set of conjugate data, 2) a fast method to replace data with its conjugate, and 3) a way to detect the first “1” bit in the TTL so the replacement process can be terminated.

To generate a set of conjugate data to perform this process, we use gain saturation, or cross-gain modulation (XGM), in a semiconductor-optical amplifier (SOA) [5], [6]. An SOA is a well-studied optical device that has a number of interesting nonlinear properties, including XGM, cross-phase modulation (XPM), and four-wave mixing (FWM). A high-power data channel (the data to be conjugated) enters the SOA through one of its input ports, and a low-power continuous-wave (CW) signal enters via the other. An illustration of this process is shown in Fig. 3. In our experiment, both channels were at the same wavelength. When the data channel is “on” (a data “1” bit), the SOA provides the majority of its gain to the high-power signal, and the CW signal is squelched, resulting in a “0” bit on the CW wave at the output of the optical circulator. However, when the data channel is “off” (a data “0” bit), the CW signal receives most of the gain and a “1” bit results at the output. The resulting circulator output carries data conjugate to that of the data channel. The extinction ratio in this process can be > 10 dB. This inverted data can then be inserted where necessary to perform the “decrementing-via-inversion” procedure.

To replace data with its conjugate (“databar”), we use difference-frequency-generation (DFG) in periodically-poled lithium-niobate (PPLN) waveguide wavelength shifters [7]. These nonlinear devices use a cascaded $\chi^{(2)} : \chi^{(2)}$ process to efficiently shift an input signal to a new wavelength when a pump signal is present. The PPLN waveguide adds negligible spontaneous emission noise, operates with little to no chirp,

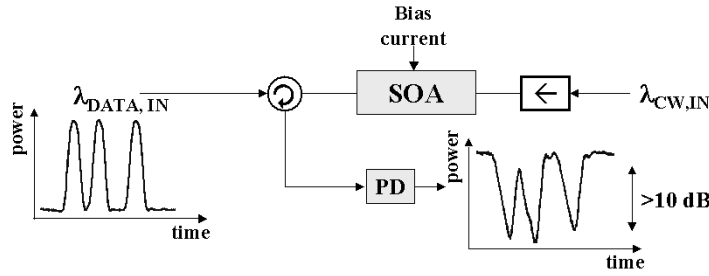


Fig. 3. Using gain saturation in a semiconductor-optical-amplifier (SOA) conjugate data can be generated at the output of the optical circulator. A high-power data channel and low-power continuous-wave (CW) channel (in our experiment, both channels were at the same wavelength) counter-propagate through the SOA. When the high-power channel is “on” (a “1” bit) the gain of the SOA is saturated and the low-power channel is strangled, resulting in a “0” bit. When the high-power channel is “off” (a “0” bit) the SOA contributes all its gain to the low-power channel, resulting in a “1” bit. The extinction ratio can be > 10 dB. Oscilloscope traces of the data sequence “10 100 100” are shown.

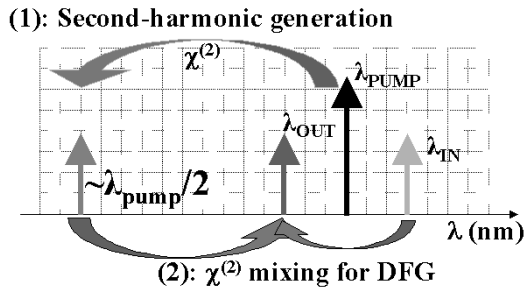


Fig. 4. Difference-frequency-generation (DFG) in a periodically-poled lithium-niobate (PPLN) waveguide results in “reflection” of an input wavelength λ_{IN} around the PPLN pump wavelength λ_{PUMP} via a cascaded $\chi^{(2)} : \chi^{(2)}$ process. First, via second-harmonic-generation, a channel at $\lambda_{PUMP}/2$ is generated (1), and that channel mixes with the input λ_{IN} to produce λ_{OUT} (2).

has similar up- and down- conversion efficiency, induces negligible crosstalk at the output, and has a $> \text{THz}$ bandwidth [7]. In essence, the output wavelength is the input wavelength “reflected” around the pump wavelength, as shown in Fig. 4. These devices have seen application in a number of optical switching architectures [8]. A PPLN waveguide only λ -shifts an input channel to a new wavelength when a pump channel is present. By using two PPLN waveguides (one for the “data” and another for the inverted data, or “databar”) and modulating the PPLN pumps appropriately, we choose when the “data” and “databar” signals are λ -shifted. Through the use of conjugate pump signals (so only one of the two signals is being λ -shifted at any given time), we can shift either “data” or “databar” as needed. By only λ -shifting “databar” to a new wavelength when the TTL-decrementing algorithm requires inverted data, and shifting “data” to a new wavelength at all other times (during the rest of the TTL, and during the rest of the packet), we can combine the resulting two λ -shifted signals and filter out the new wavelength, resulting in a combined, λ -shifted, and TTL-decremented channel.

III. EXPERIMENTAL SETUP

The experimental setup of our optical TTL module is shown in Fig. 5. An incoming data packet with an 8-b TTL field (with a 1-b guard time before the TTL to ensure proper timing, but no guard time between the end of the TTL and the rest of the packet data) is amplified and split into three branches. The middle

branch is the “data” sent to the “data” PPLN waveguide. The top branch is transmitted through a 3-port optical circulator and through the SOA to create “databar” via gain saturation, as described in the preceding section. The final branch of the input data stream is sent to a receiver to check for the first “1” bit within the TTL. This is done by using this signal (after an OR gate) as the clock input to a D-flip-flop (DFF) that selects which of the data streams (“data” or “databar”) is λ -shifted at any given moment by the PPLN waveguides by modulating the PPLN waveguide pumps. The DFF is the key to controlling the TTL-decrementing algorithm, with the “Q” output of the DFF controlling the “databar” PPLN pump modulator and the “Qbar” output controlling the “data” PPLN pump modulator. This guarantees that only one of the two data streams will be λ -shifted at any given moment. A “TTL start” pulse (which we generate artificially, but could be provided by some sort of optical preamble detection such as in [9]) signals to the electronics the start of the TTL field and acts as both an input and a clock (after the OR gate) to the DFF, setting the “Q” output of the DFF to “1.” As the “Q” output of the DFF is tied to the “databar” PPLN pump, the “databar” channel is shifted to a new wavelength, λ_{OUT} , while the “Q” output remains high. The “data” channel, whose PPLN pump is controlled by the “Qbar” output of the DFF, is not λ -shifted while the “Q” output remains high. We shift “databar” to λ_{OUT} only when the TTL-modification requires the conjugate data at the output – that is, until the first “1” bit is present in the TTL. The first “1” bit in the TTL of the packet acts as a “clock” input to the DFF, resetting the “Q” output to zero, shutting off the pump to the “databar” PPLN and turning on the pump to the “data” PPLN. This begins λ -shifting of the “data” stream to λ_{OUT} . After filtering out all wavelengths save λ_{OUT} , the two synchronized λ -shifted signals (“data” and “databar,” which are never λ -shifted to λ_{OUT} simultaneously) are combined via a coupler and the result is a TTL-modified λ_{OUT} output channel.

The “Q” signal is also used to control a switch that drops the packet when the TTL of the incoming packet is zero. Any “1” bit within the TTL field will cause the “Q” output to reset to “0.” Thus, if the “Q” output is still “1” after the TTL has passed, then there cannot be any “1” bits within the TTL field – it must have a zero value. A simple decision circuit is used to test “Q” immediately after the TTL has passed through the module. Only if all TTL bits are zero will the “Q” output remain high,

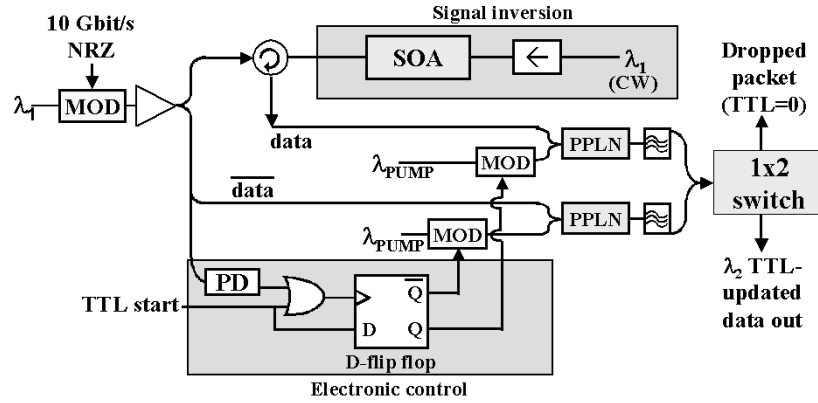


Fig. 5. The experimental setup of our optical TTL module. The initial data stream is split into three branches – one is used to create conjugate data (“databar”) via gain saturation in an SOA, one is used as the data stream, and the last is detected and used to control the module. A D-flip-flop controls the insertion of conjugate data by controlling the PPLN waveguide pumps. The “TTL start” pulse is perhaps generated by an existing preamble-detection module, such as described in [9].

and thus a positive decision at this stage can be used to reset the DFF and drive an optical switch that drops the packet. While it is possible that some corruption of packet data can take place if the TTL is zero (as the “Q” output remains high, and thus “databar” is shifted to λ_{OUT} after the TTL has passed – it is for this reason that the decision circuit also resets the DFF), as packets are dropped or destroyed when the TTL of an incoming packet is zero, the resulting packet data may be irrelevant. In addition, as any packet with a nonzero TTL will stop shifting “databar” and begin shifting the “data” stream by the end of the TTL field, no guard time between the end of the TTL and the rest of the packet is required.

IV. RESULTS AND DISCUSSION

Three 20-B packets, each with varying TTL values and payload data, 0.8 ns of guard time between packets, and no guard time between the end of the TTL and the rest of the packet, are modulated at 10 Gb/s onto a 1545 nm signal wavelength (λ_{IN}) and sent into the TTL module. Two packets had nonzero TTL values, and one packet had a zero TTL. A closer look at the TTL field of one of the packets is shown in Fig. 6 for a packet with a TTL value of “00 111 100” (from the LSB to the most-significant-bit, or MSB). The input “data” signal for this packet is shown in Fig. 6(a), and the “databar” signal generated via gain saturation in the SOA (with a 1545 nm CW input to the SOA) is shown in Fig. 6(b). The SOA bias current was 80 mA. The first three bits of the TTL (starting with the LSB) are “001”, so the first “1” bit is the third bit of the TTL. Thus, the “decrementing-via-inversion” algorithm should require that the first 3 bits of the TTL be replaced by their conjugates. The PPLN pump modulation signals (the outputs of the DFF) are shown in Fig. 6(c) and (d), where (c) is the “Qbar” signal modulating the “data” PPLN pump, and (d) is the “Q” signal modulating the “databar” PPLN pump. The pump wavelength for both PPLN’s was 1550.12 nm, and the pump power into each PPLN was approximately 10 dBm, resulting in a λ -shifted output signal at 1555.24 nm at around -13 dBm. The PPLN spectrum showing the input, pump, and λ -shifted output is shown in Fig. 7. After amplification and filtering to remove all wavelengths save 1555.24 nm, the resulting streams were

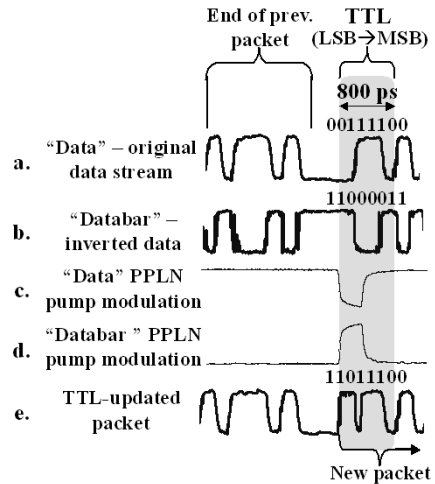


Fig. 6. A close-up look at the TTL field of a packet with a TTL value of “00 111 100” (LSB \rightarrow MSB) (a) The TTL field of the “data” stream. (b) The TTL field of “databar”, the conjugate data generated via gain saturation in the SOA, showing a TTL value of “11 000 011”. (c) The control signal for the “data” PPLN pump – “data” is λ -shifted at all times save from the start of the TTL until after the first “1” bit in the TTL. (d) The control signal for the “databar” PPLN pump – “databar” begins λ -shifting when the TTL starts and ends immediately after the first “1” bit. (e) A close-up view of the TTL-modified output, with a new TTL of “11 011 100” (the bits up to and including the first “1” bit having been replaced with “databar”).

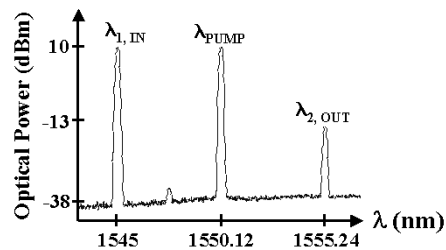


Fig. 7. The optical spectrum at the output of the “data” PPLN waveguide, showing the three signals – “data” at $\lambda_{IN} = 1545$ nm, the λ -shifted output at $\lambda_{OUT} = 1555.24$ nm, and the PPLN pump at 1550.12 nm.

combined and the TTL-modified result is shown in Fig. 6(e). While our technique results in a shift of the input wavelength to a new output wavelength, this can be alleviated by using a conventional wavelength shifter (or a second PPLN with a

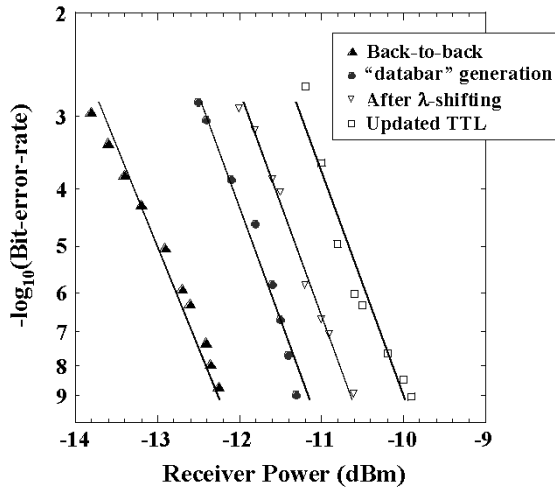


Fig. 8. Power penalty curves for the optical TTL module. The total penalty is ~ 2.4 dB.

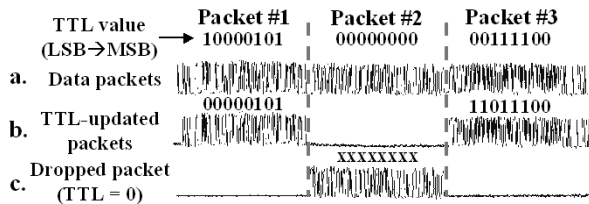


Fig. 9. (a) A number of input packets to the TTL module, each with different TTL values and data payloads. (b) Packets with nonzero TTL values have their TTL decremented and then pass through the module. (c) When a packet with a TTL value of zero enters the module, a signal is generated that drops the packet from the network (in this case using an optical switch), and the resulting TTL value is irrelevant.

pump wavelength identical to the first, causing a second shift around the pump back to the original input wavelength).

Power penalty curves are shown in Fig. 8. The total power penalty of our module is ~ 2.4 dB when compared to the back-to-back receiver sensitivity at 10^{-9} bit-error-rate, arising mostly by the λ -shifting in the PPLN and insertion of parts of “databar” into the TTL field. Using the current setup, it may be difficult to traverse a large number of hops, and as such this system may be viable only in a low-hop network core. However, the use of a higher-efficiency PPLN waveguide and an SOA with lower chirp (or an optical 2/3 R regenerator) may reduce these sensitivity penalties considerably, allowing for transmission beyond a handful of nodes.

To demonstrate packet dropping, a lithium-niobate optical switch was placed in series with the TTL module and controlled by a decision circuit as described in the above section. The circuit checks the “Q” value on the first bit after the TTL ends – if it is high, it signals the switch to drop the packet and resets the DFF so that the “Q” signal is again “0.” The three packets are shown in Fig. 9(a), and the switch outputs are shown as Fig. 9(b) and (c), where (b) shows the “through” port and (c) the “drop” port.

While this technique was demonstrated for NRZ-modulated data, this technique can be applied to RZ systems without major modification. By replacing the low-power CW signal input to the SOA with a constant stream of RZ “1” bits synchronized to

the input, a “databar” stream of RZ bits can be generated. The rest of the system requires no modification—such an implementation also removes the need for the 1-bit guard time prior to the start of the TTL. Without further investigation as to the effects of the PPLN and SOA on other types of signals (CRZ, CSRZ), we are unable to say whether this technique can be extended to other modulation formats.

V. SUMMARY

“Routing loops,” where a misdirected packet is routed in circles indefinitely, can be a significant problem in modern-day networks, leading to severe network congestion if misrouted packets are not quickly dropped from the network. To combat this problem, IP packets incorporate a “time-to-live” field that is decremented by one at each “hop” within the network – when the TTL value reaches zero, the packet is dropped from the network. To begin a move toward all-optical routing within the network core, it may be necessary to implement this TTL decrementing process in the optical domain.

We demonstrate an optical TTL decrementing module that acts upon a standard 8-b binary TTL field located within an optical packet. If the value of the TTL field within the packet is nonzero, the TTL is decremented via a decrementing-via-inversion process and the packet passes through the module, perhaps toward an optical switch fabric. If the TTL field is zero, however, the module generates a signal that can be used to drop or destroy the packet. Our technique uses gain saturation within an SOA to generate conjugate data, and difference-frequency-generation in a PPLN to enable wavelength conversion and insertion of conjugate data. This technique is independent of the length of the TTL and packet, requires minimal electronic control, requires no guard time between the end of the TTL field and the rest of the packet, and has only a 2.4-dB power penalty at 10^{-9} BER.

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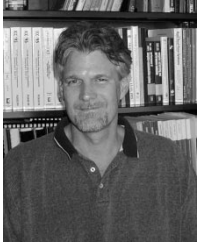


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